SCT CONFIDENTIA

SCT CON

SCT CON

SCT CON

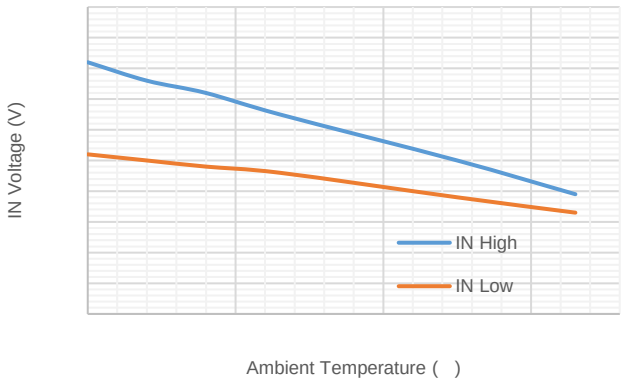


Figure 3. IN Threshold VS Temperature

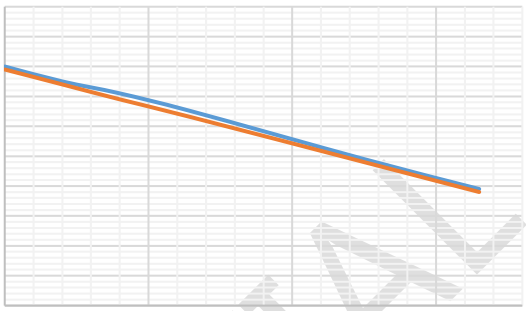


Figure 4. DIAG_EN Threshold VS Temperature

Figure 5. Body-Diode Forward Voltage VS Temperature

Figure 6. $I_{SHUTDOWN}$ vs Input Voltage

Figure 7. I_Q vs Input Voltage

SCT CONFIDENTIAL

 © 2024 Silicon Content Technology Co., Ltd. All Rights Reserved

SCT CONFIDENTIAL

SCTC

S

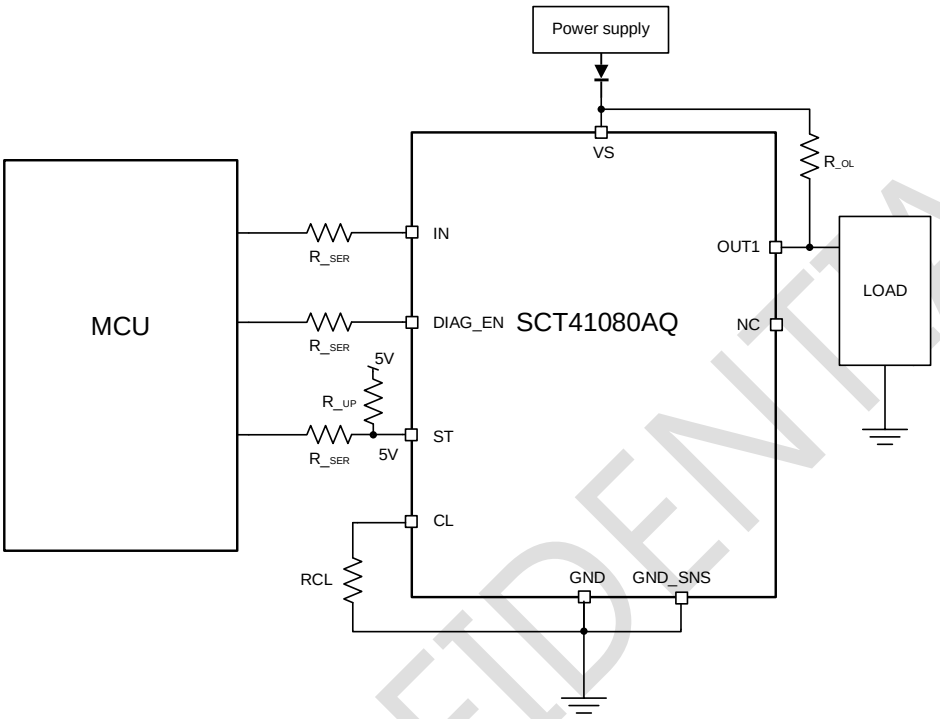


Figure 17. Version A Application Schematic

Input Voltage	13.5V Normal 8V to 18V
Load Current	Typical 4A
Current Limit	5A
MCU Voltage	5V

SCT CONFIDENTIAL

 © 2024 Silicon Content Technology Co., Ltd. All Rights Reserved

The PCB layout is very important to good PCB design can optimize heat transfer, which is absolutely essential for the long-term reliability of the device.

1. Maximize the copper coverage on the PCB to increase the thermal conductivity of the board. Maximum copper is extremely important when there are not any heat sinks attached to the PCB on the other side of the package.
2. More vias should be placed below the device Thermal PAD to further] l ’ of te M ę u

SCT CONFIDENTIAL

SCT CONFIDENTIAL

 © 2024 Silicon Content Technology Co., Ltd. All Rights Reserved