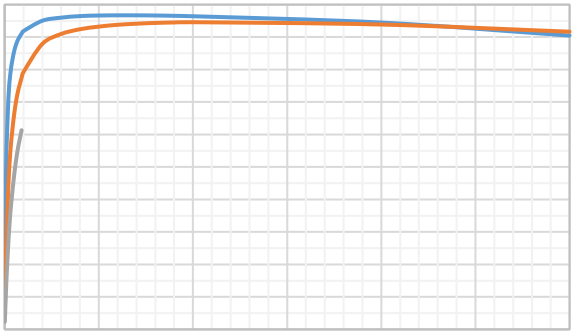
3.8V-30V Vin, 3A, High Efficiency Synchronous Step-down DCDC Converter

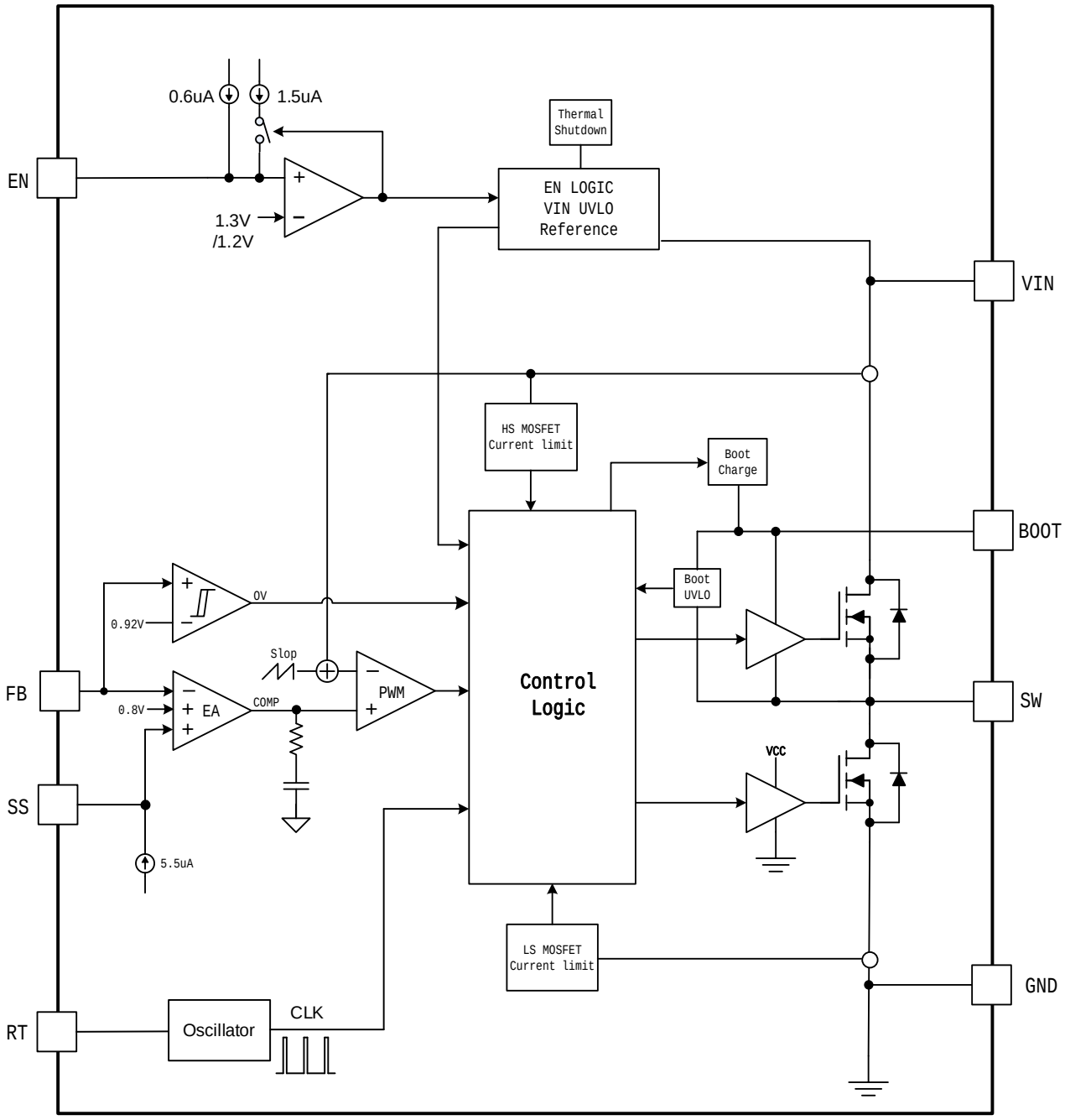
x

&

		,		

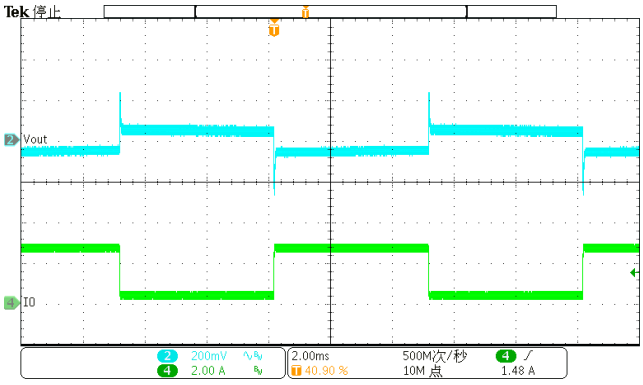
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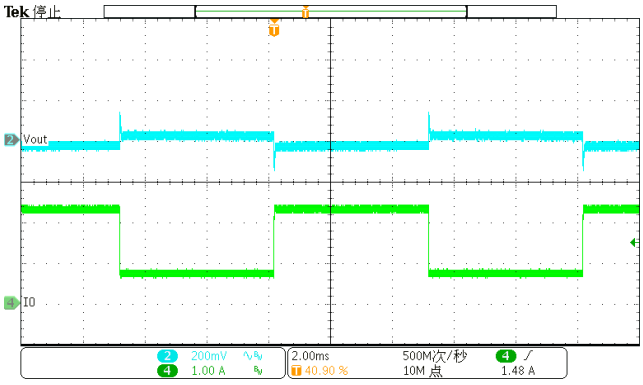


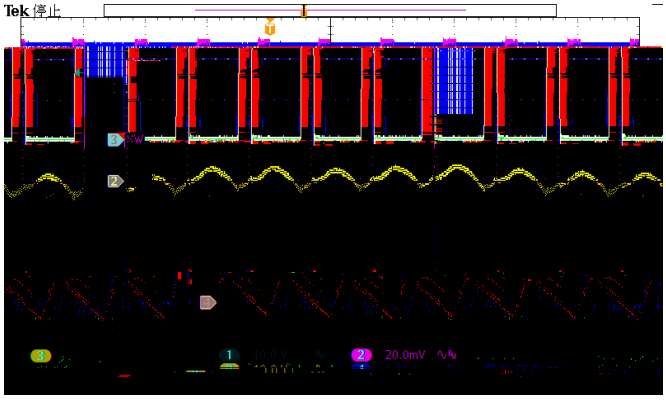

0.6uA
1.5uA
Thermal Shutdown
EN
1.3V /1.2V
EN LOGIC VIN UVLO Reference
VIN
HS MOSFET Current limit
Boot Charge
BOOT
OV
0.92V
Boot UVLO
Slop
FB
0.8V
EA
COMP
PWM
Control Logic
SW
VCC
SS
5.5uA
LS MOSFET Current limit
GND
RT
Oscillator
CLK

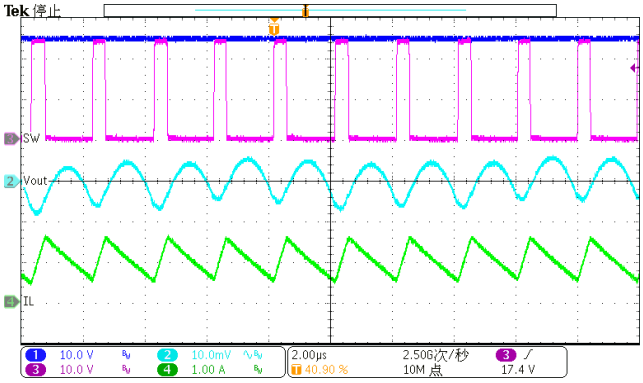
.ì¬ÑØ®Í šàen-US S U R %8„!c 2 Q G X F W R U 6 H ‘ a''ç”d¢sp0 S U SˆB 0 .ì¬Ña® F H’•J’7 S U R 5%A 0 K H U H

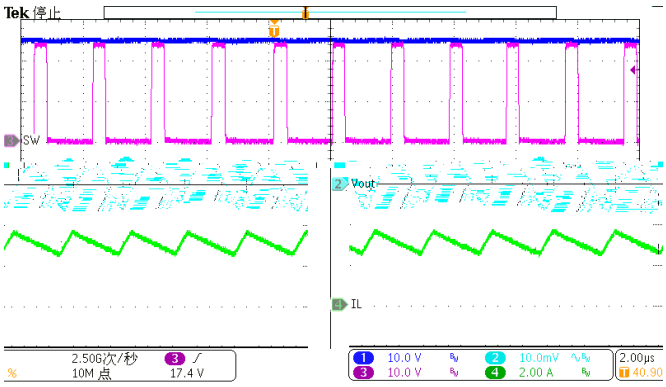
VOUT	FREQUENCY	R1	R2	R_{RT}	L1		COUT
5V	500kHz	53.6 kΩ	10.2 kΩ	Floating	6.8 uH	Optional	44uF
12V	500kHz	143 kΩ	10.2 kΩ	Floating	15 uH	Optional	44uF
5V	1.15M						


Tek 停止
Vout
IO
200mV
2.00 A
2.00ms
40.90 %
500M次/秒
10M 点
1.48 A

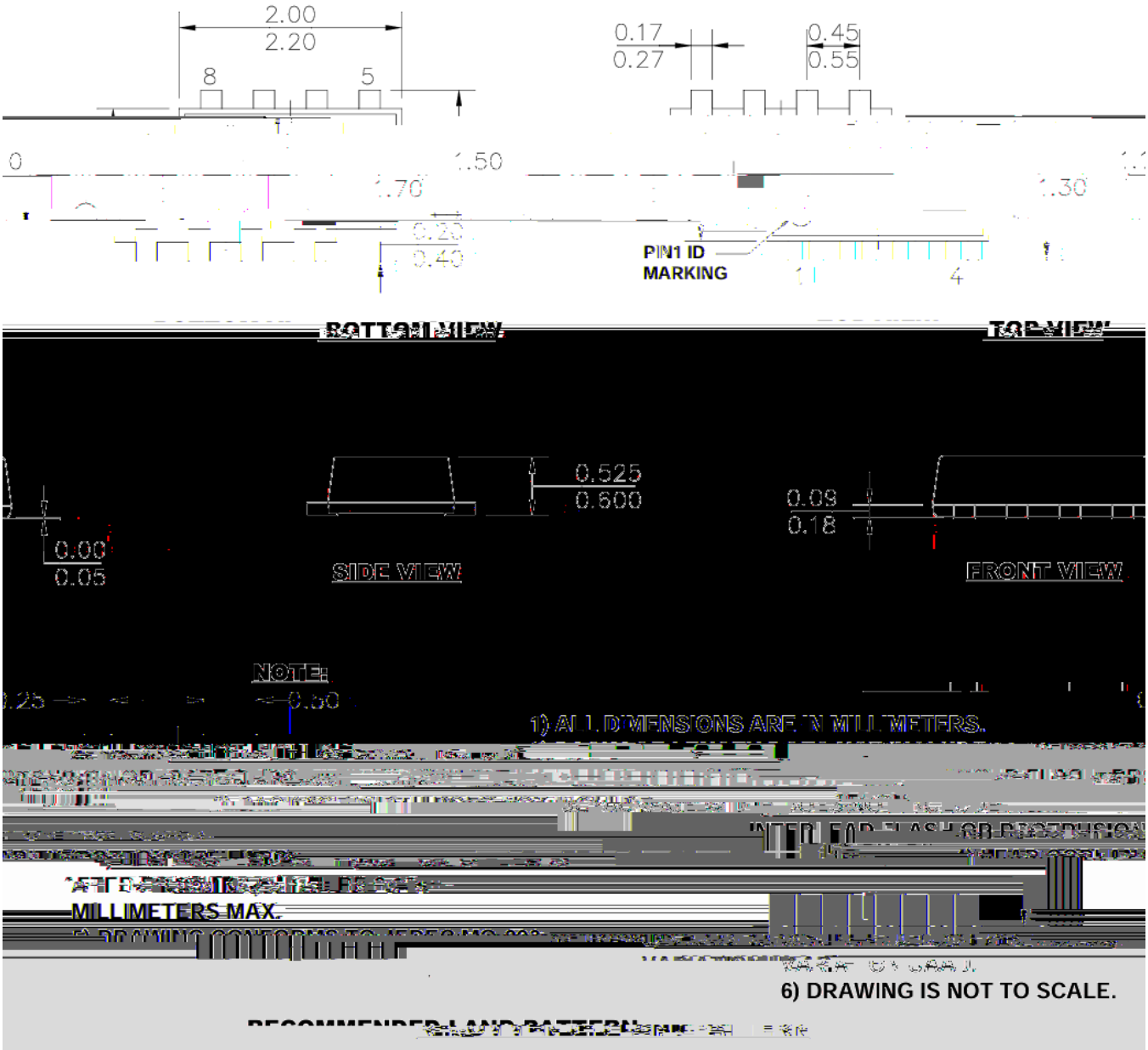

Tek 停止
Vout
IO
200mV
1.00 A
2.00ms
40.90 %
500M次/秒
10M 点
1.48 A

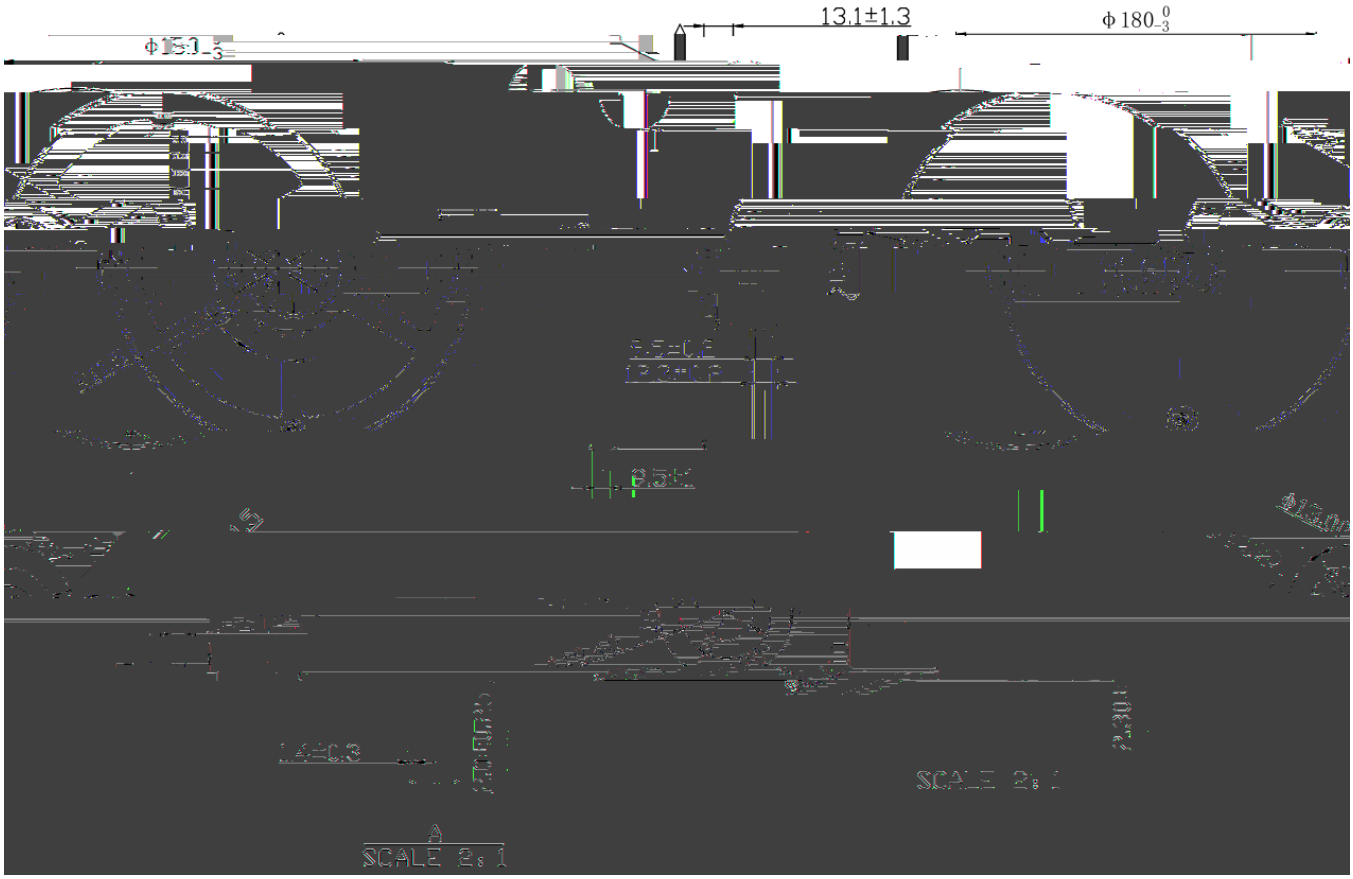

Tek 停止
SW
20.0mV

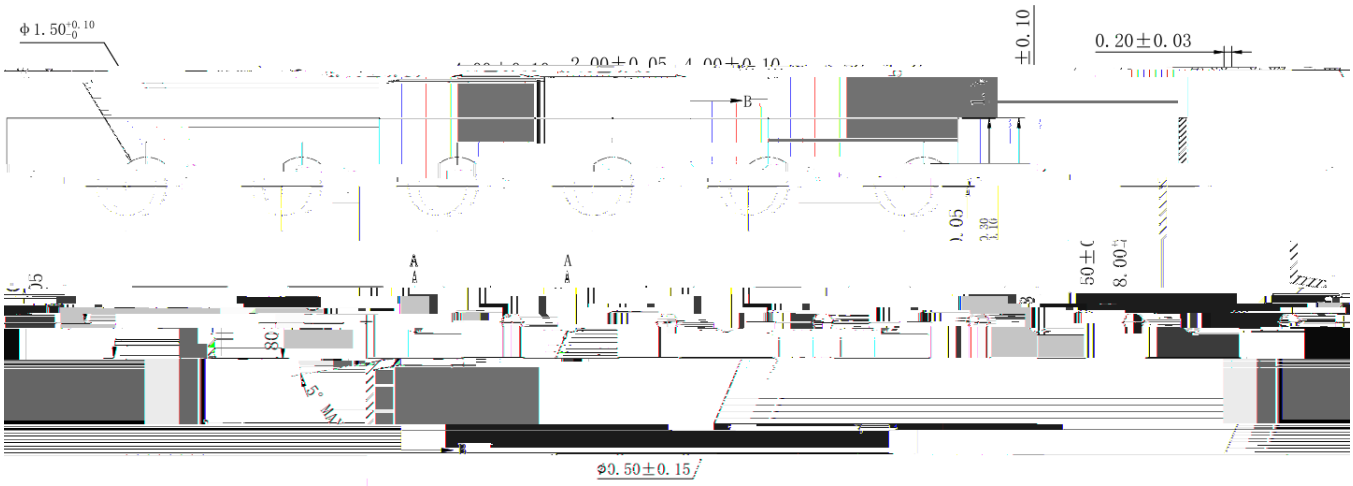

Tek 停止
SW
Vout
IL
10.0 V
10.0 V
10.0mV
1.00 A
2.00µs
40.90 %
2.50G次/秒
10M 点
17.4 V


Tek 停止
SW
Vout
IL
2.50G次/秒
10M 点
17.4 V
10.0 V
10.0 V
10.0mV
2.00 A
2.00µs


FLUKE
72.7°C
自动 1
78.3
24.1
ε=0.95
BG=25.0
01:08:45 PM

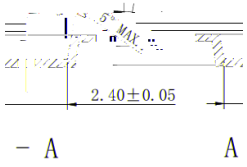

2.00
2.20
8
5
0.17
0.27
0.45
0.55
0
1.50
1.70
1.30
0.20
0.40
PIN1 ID
MARKING
1
4
0.525
0.600
0.00
0.05
SIDE VIEW
0.09
0.18
FRONT VIEW
NOTE:
1) ALL DIMENSIONS ARE IN MILLIMETERS.
MILLIMETERS MAX.
6) DRAWING IS NOT TO SCALE.


13.1±1.3
SCALE 2:1
SCALE 2:1

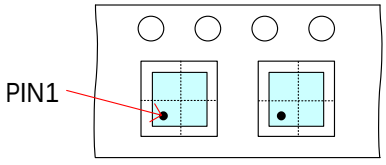

0.20±0.03
±0.10
B
A
A
50±0
8.00
3
1.05
3.38
3.10
0.38
5° MAX.
Φ0.50±0.15

0.70±0.05

B – B


5° MAX.
2.40±0.05

– A A


PIN1